Complete the following problems from the textbook:

- Appendix B: B.5, B.11.
- 4.1.1 (This problem uses Figure 4.2 on page 247 in your text. Assume that the seven output control lines are named, from top to bottom, RegWrite, MemRead, Reg-ALU-MUX, MemWrite, ALUOperation, Reg-MEM-MUX, and Branch.), 4.1.2, 4.2.1, 4.2.2, 4.2.3, 4.5.1, 4.8.1, 4.8.2 (for 4.8.1 and 4.8.2, ignore transfer time), 4.8.3.